Sir:

## PATENT APPLICATION

## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In Re Application Of	) Group Art Unit: 2818	GENTRAL FAX CENTER
Ken Takeuchi et al.	) Examiner: H. Ho	AUG 0 5 2004
Serial No.: 10/073,999	)	
Filed: February 14, 2002	) Confirmation No. 9741	OFFICIAL
For: Nonvolatile Semiconductor Memory Having Plural Data Storage Portions For A Bit Line Connected To Memory Cells	) Atty. Dkt. No. 001701.00140 )	
MAIL STOP AF Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450	This paper of 11 pages is being faxed to 703 on Appust 5, 2004  Christopher R. Glembocki  Reg. No. 38,800	872 9306

## **AMENDMENT**

In response to the final Office Action mailed May 11, 2004, please amend the instant application as follows:

Amendments to the Claims are reflected in the Listing of Claims, which begins on page 2 of this paper.

Remarks begin on page 9 of this paper.